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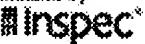
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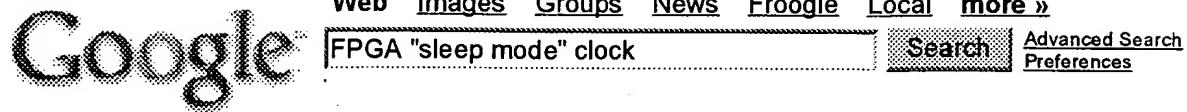
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An integrated Watchdog Timer with it's own **clock** signal provides protection ...

If the device is in **SLEEP mode** the WDT timeout causes the device to wake-up ...

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DRPIC1655X low-cost, high performance, 8-bit, fully static soft IP ...

So most instructions are executed within 1 system **clock** period, ... If the device

is in **SLEEP mode** the WDT timeout causes the device to wake-up and continue ...

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FPGA external clock (Xilinx XC4020XL). Radio power and operating mode (Bluetooth)
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